

**S/N 10/612,793**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Arup Bhattacharyya

Examiner: Thomas Dickey

Serial No.: 10/612,793

Group Art Unit: 2826

Filed: July 2, 2003

Docket: 1303.111US1

Confirmation No.: 5437

Title: HIGH-PERFORMANCE ONE-TRANSISTOR MEMORY CELL

---

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicant hereby authorizes the Commissioner to charge the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p), to Deposit Account No. 19-0743. Please charge any additional fees or credit any overpayment to Deposit Account No. 19-0743.

Applicant brings to the attention of the Examiner the following Office Actions and responses associated with one or both of the inventors of the instant application and/or the assignee of the instant application. This material is available and accessible at the United States Patent and Trademark Office.

**GATED LATERAL THYRISTOR-BASED RANDOM ACCESS MEMORY CELL (GLTRAM)**

Application Serial No. 11/209,228 (Atty. Ref. 1303.072US2) Notice of Allowance mailed 07-21-08, 5 pg

**ONE TRANSISTOR SOI NON-VOLATILE RANDOM ACCESS MEMORY CELL**

Application Serial No.11/158,744 Atty Ref(1303.080US3),Non- Final Office Action mailed 08-21-08, 20 pgs

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Serial Number: 10/612,793

Filing Date: July 2, 2003

Title: HIGH-PERFORMANCE ONE-TRANSISTOR MEMORY CELL

Page 2

Dkt: 1303.111US1

Pursuant to 37 C.F.R. 1.98(a)(2), Applicant believes that copies of cited U.S. Patents and Published Applications, and Non-Published Applications identifiable by USPTO Serial Number, are no longer required to be provided to the Office. Notification of this change to this effect was provided in the United States Patent and Trademark Office OG Notice dated October 19, 2004. Thus, Applicant has not included copies of any US Patents or US Patent Applications identifiable by serial number that may be cited with this submission. Should the Office require copies to be provided, Applicant respectfully requests that notice of such requirement be directed to Applicant's below-signed representative. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

SCHWEGMAN, LUNDBERG & WOESSNER, P.A.  
P.O. Box 2938  
Minneapolis, MN 55402  
(612) 349-9587

Date

10 Nov. '08

By



Timothy B. Clise

Reg. No. 40,957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 10th day of November, 2008.

Name

Ryan Sanders

Signature

